

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: D. HISAMOTO, et al.
Application No.: Not Assigned Yet
Filed: On Even Date Herewith
For: FABRICATION METHOD AND STRUCTURE OF
SEMICONDUCTOR NON-VOLATILE MEMORY DEVICE
Art Unit: Not Assigned Yet
Examiner: Not Assigned Yet

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

December 4, 2003

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims

Remarks are included following the amendments